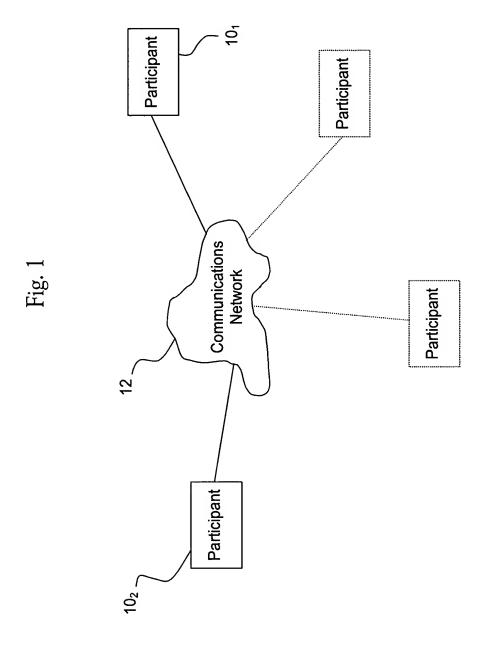
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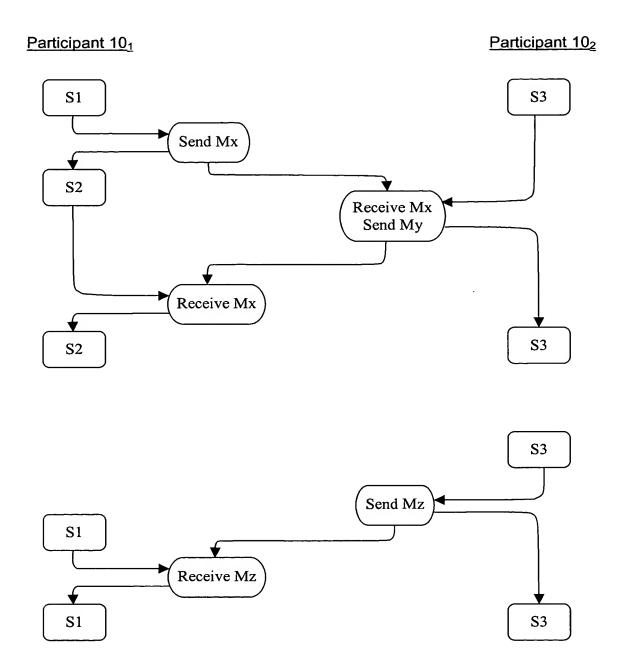
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Fig. 2



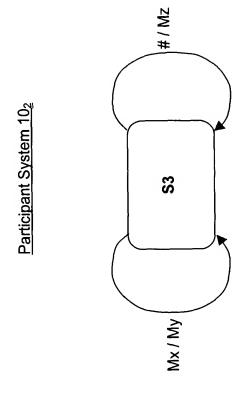
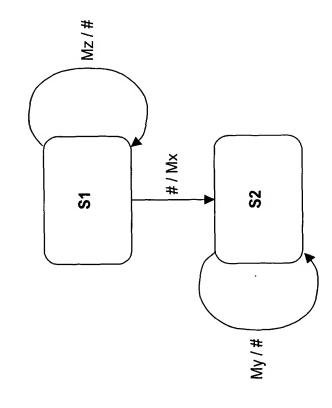


Fig. 3

Participant System 10<sub>1</sub>



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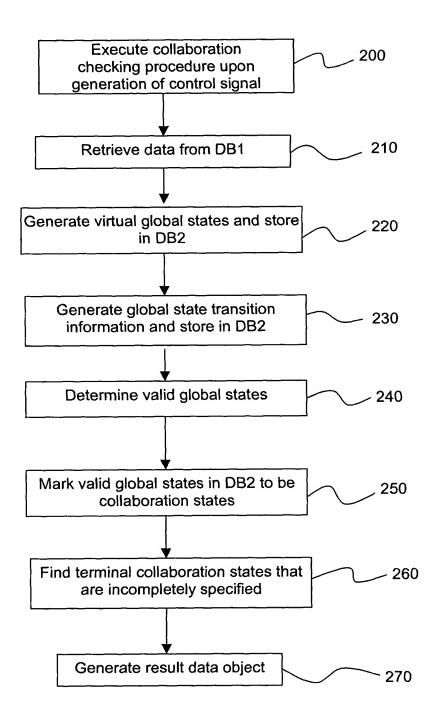
Fig. 4 110 DB1 DB2 DB3 Memory DB4 **Processor** CPP 130 140 150 Output Input device device 170 - 160 180 \_120 100 112 111 -

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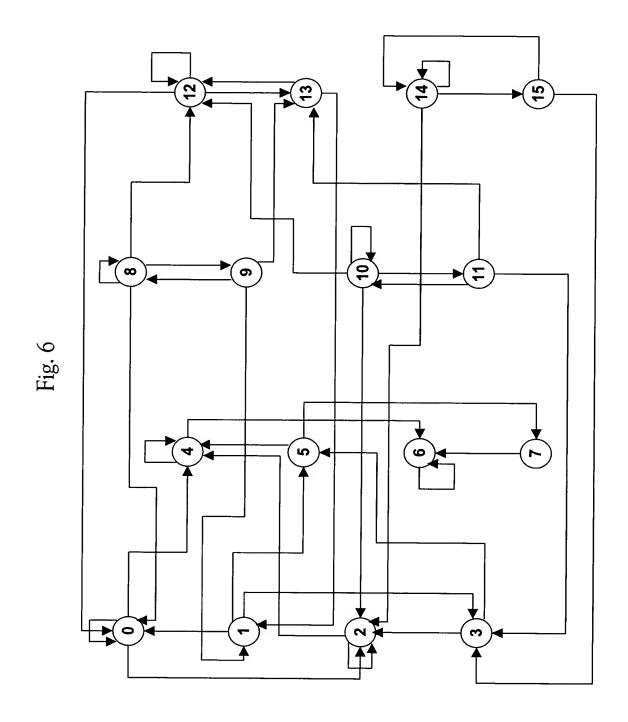
FOR A COLLABORATIVE PROCESS

Fig. 5



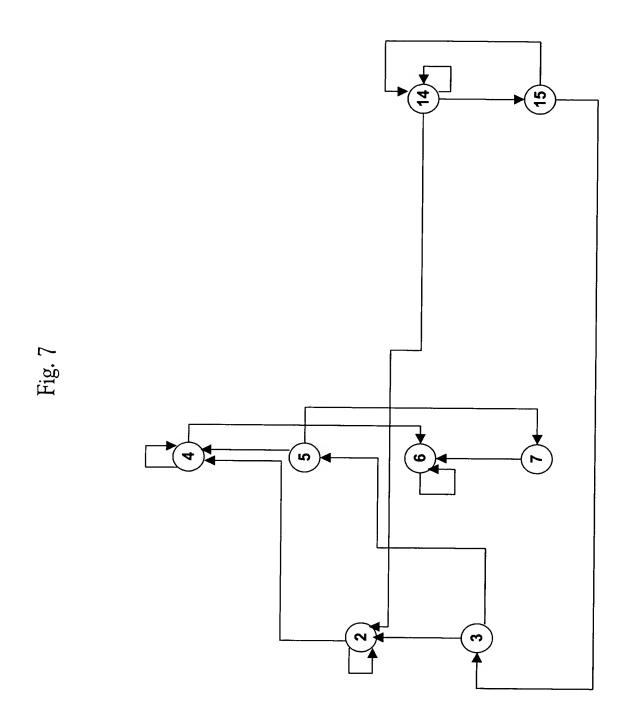
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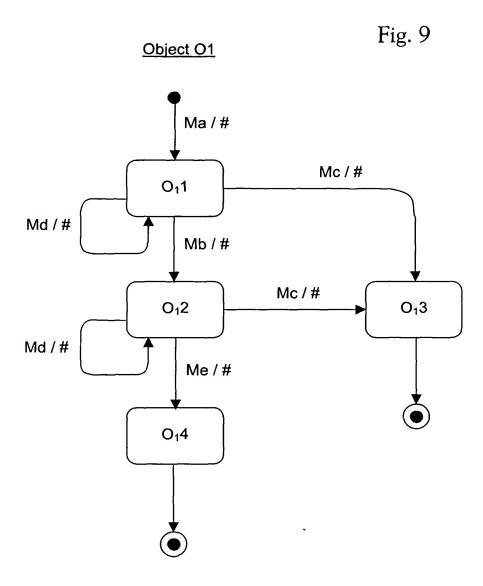
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Fig. 8

0	invalid	dead-end	complete	S2	S3	Mx: 1	My: 1	Mz: 1
1	invalid	dead-end	complete	S2	S3	Mx: 1	My: 1	Mz: 0
2	valid	live-end	complete	S2	S3	Mx: 1	My: 0	Mz: 1
3	valid	live-end	complete	S2	S3	Mx: 1	My: 0	Mz: 0
4	valid	live-end	complete	S2	S3	Mx: 0	My: 1	Mz: 1
5	valid	live-end	complete	S2	S3	Mx: 0	My: 1	Mz: 0
6	valid	dead-end	incomplete	S2	<b>S</b> 3	Mx: 0	My: 0	Mz: 1
	AO1	AO2	A03		-			

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SUPPORT IN DEVELOPING A PROCESS SPECIFICATION FOR A COLLABORATIVE PROCESS



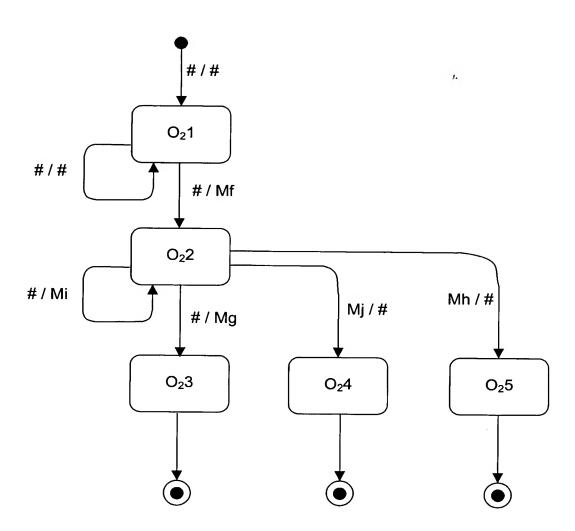
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Fig. 10

## Object O2

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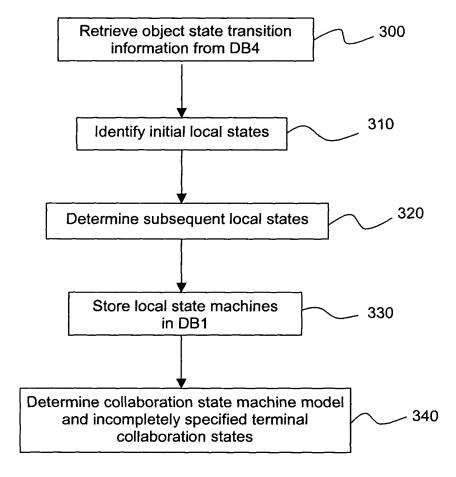
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Fig. 11

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Fig. 12

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